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ABSTRACT

A digital system for Bit Error Rate Testing (BERT) of test transmission lines and/or devices and including a test equipment coupled to at least one transmission line for transferring digital information between the test equipment and a company office, a bit error generator coupled to the transmission lines for generating test patterns, a device for storing a plurality of predetermined profiles, each profile having associated therewith at least one parameter for measuring the integrity of the test equipment or the transmission lines, the parameters including one of the generated test patterns. The predetermined profiles are automatically sent through the test equipment and the transmission lines in a predetermined order thereby avoiding user interaction, the test equipment receives information responsive to the profiles sent through the transmission line, wherein the received information is verified against the information included in the profiles for determining the integrity of the test equipment or the transmission lines.